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**IN THE CLAIMS:**

Please substitute the following claims for the same numbered claims in the application.

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What is claimed is:

1. (Currently Amended) A method of evaluating an integrated circuit manufacturing process, said method comprising:
  - determining the actual film profile of a film formed in said integrated circuit manufacturing process by plotting multiple thickness measures across said film;
  - comparing said actual film profile with a desired film profile; and
  - if said actual film profile does not match said desired film profile, adjusting said integrated circuit manufacturing process to make said actual film profile match said desired film profile.
2. (Original) The method in claim 1, wherein said desired film profile comprises a range of acceptable film profiles.
3. (Original) The method in claim 1, wherein said actual film profile and said desired film profile comprise profiles across cross-sections of said film.
4. (Original) The method in claim 1, further comprising repeating said determining, comparing, and adjusting processes for different cross-sections of said film.
5. (Currently Amended) The method in claim 1, further comprising A method of evaluating an integrated circuit manufacturing process. said method comprising:
  - determining the actual film profile of a film formed in said integrated circuit manufacturing process;
  - determining said a desired film profile by averaging previous formations of said film formed using the same integrated circuit manufacturing process;
  - comparing said actual film profile with said desired film profile; and

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if said actual film profile does not match said desired film profile, adjusting said integrated circuit manufacturing process to make said actual film profile match said desired film profile.

6. (Currently Amended) The method in claim 5, further comprising A method of evaluating an integrated circuit manufacturing process, said method comprising:

determining the actual film profile of a film formed in said integrated circuit manufacturing process;

determining said a desired film profile by averaging previous formations of said film formed using the same integrated circuit manufacturing process wherein said averaging process comprises a statistical analysis;

comparing said actual film profile with said desired film profile; and

if said actual film profile does not match said desired film profile, adjusting said integrated circuit manufacturing process to make said actual film profile match said desired film profile.

7. (Original) The method in claim 1, wherein said actual film profile and said desired film profile are irregular.

8. (Original) A method of evaluating an integrated circuit manufacturing process, said method comprising:

plotting multiple thickness measures taken at regular intervals along a line crossing a film formed in said integrated circuit manufacturing process to produce an actual film profile of said film;

comparing said actual film profile with a desired film profile; and

if said actual film profile does not match said desired film profile, adjusting said integrated circuit manufacturing process to make said actual film profile match said desired film profile.

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9. (Original) The method in claim 8, wherein said desired film profile comprises a range of acceptable film profiles.
10. (Original) The method in claim 8, wherein said actual film profile and said desired film profile comprise profiles across cross-sections of said film.
11. (Original) The method in claim 8, further comprising repeating said determining, comparing, and adjusting processes for different cross-sections of said film.
12. (Original) The method in claim 8, further comprising determining said desired film profile by averaging previous formations of said film formed using the same integrated circuit manufacturing process.
13. (Original) The method in claim 12, wherein said averaging process comprises a statistical analysis.
14. (Original) The method in claim 8, wherein said actual film profile and said desired film profile are irregular.
15. (Original) A method of evaluating an integrated circuit manufacturing process, said method comprising:  
recording multiple thickness measures taken at regular intervals along a plurality of lines crossing a plurality of different sample production runs of the same film formed in said integrated circuit manufacturing process;  
plotting said thickness measures to produce a plurality of sample film profiles of said film;

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averaging said sample film profiles in a statistical process to produce a desired film profile;

plotting multiple thickness measures taken at regular intervals along at least one line crossing an actual production film formed in said integrated circuit manufacturing process to produce an actual film profile of said film;

comparing said actual film profile with said desired film profile; and

if said actual film profile does not match said desired film profile, adjusting said integrated circuit manufacturing process to make said actual film profile match said desired film profile.

16. (Original) The method in claim 15, wherein said desired film profile comprises a range of acceptable film profiles.

17. (Original) The method in claim 15, wherein said actual film profile and said desired film profile comprise profiles across cross-sections of said film.

18. (Original) The method in claim 15, further comprising repeating said comparing and adjusting processes for different cross-sections of said film.

19. (Original) The method in claim 15, wherein said actual film profile and said desired film profile are irregular.